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DEC 20 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Harer et al.

Application No. **09/760,063**

Filed: **01/12/01**

For: **SIMULATION-BASED
FUNCTIONAL VERIFICATION OF
MICROCIRCUIT DESIGNS**

Art Unit **2123**

Examiner: **Thomas H. Stevens**

Confirmation No. **2596**

Attorney Docket: **06816.0173 (was
4000/9)**

Response

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed Jun. 30, 2005, applicants submit the present Response on Dec. 19, 2005.